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Docket Number: 2207/10121 Total number of pages: 11
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Examiner Shane F. GERSTL	U.S. Patent and Trademark Office	703-872-9306	

Message:

Application No. : 09/751,762

Confirmation No. 2183

Applicant : Sailesh KOTTAPALLI et al.

Filed : December 29, 2000

Title : METHOD FOR CONVERTING PIPELINE STALLS CAUSED BY INSTRUCTIONS WITH LONG LATENCY MEMORY ACCESSES TO PIPELINE FLUSHES IN A MULTITHREADED PROCESSOR WHERE THE INSTRUCTIONS ARE RE-EXECUTED UPON COMPLETION OF THE ACCESSES

TC/A.U. : 2183

Examiner : Shaen F. GERSTL

PAPER(s) ENTITLED: Request for Extension of Time (in duplicate) 2 pages

Response 8 pages

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